

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) A liquid crystal display device comprising:
 - a liquid crystal panel including a first substrate and a second substrate, the first substrate having a plurality of source pads and gate pads, the firsts and second substrates being attached;
 - a first printed circuit board connected to the plurality of source pads, the first printed circuit board applying signals to the source pads;
 - a second printed circuit board connected to the plurality of gate pads, the second printed circuit board applying signals to the gate pads; and
 - a plurality of gate transmitting lines connecting the gate pads with the source pads, the plurality of gate transmitting lines transmitting signals from the first printed circuit board to the second printed circuit board via the gate transmitting lines,
wherein a first gate transmitting line of the plurality of gate transmitting lines has a first resistance, wherein gate transmitting lines other than the first gate transmitting line have a second resistance, and wherein the first resistance is less than the second resistance ~~one of the gate transmitting lines has a resistance of below 30 Ω and is capable of transmitting a gate low voltage.~~
2. (Original) The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines include at least eight signal lines for transmitting signals from the first printed circuit board to the second printed circuit board.

3. (Original) The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines include common voltage signal line.

4. (Original) The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a gate high voltage signal line.

5. (Original) The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines a includes a gate low voltage signal line.

6. (Original) The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a first control signal line and a second control signal line.

7. (Original) The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a power line and a ground line.

8. (Original) The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a drive IC control signal line.

9. (Original) The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a common voltage signal line, a gate high voltage signal line, a gate low voltage signal line, first and second control signal lines, a power line and a ground line.

10. (Original) The liquid crystal display device of claim 1, further comprising a plurality of dummy pads between adjacent source pads and between adjacent gate pads.

11. (Original) The liquid crystal display device of claim 1, further comprising a plurality of dummy pads between adjacent gate pads.

12. (Original) The liquid crystal display device of claim 1, further comprising a plurality of dummy pads between adjacent source pads.

13. (Original) The liquid crystal display device of claim 1, wherein the plurality of gate transmitting lines are formed directly on the first substrate.

14. (Currently Amended) A liquid crystal display device comprising:
a liquid crystal panel including a substrate, the substrate having a plurality of source pads and gate pads;

a first printed circuit board connected to the plurality of source pads, the first printed circuit board applying signals to the source pads;

a second printed circuit board connected to the plurality of gate pads, the second printed circuit board applying signals to the gate pads; and

a plurality of gate transmitting lines formed directly on the substrate and connecting the gate pads with the source pads, the plurality of gate transmitting lines transmitting signals from the first printed circuit board to the second printed circuit board via the gate transmitting lines,
wherein the plurality of gate transmitting lines comprise two resistances.

15. (Original) The liquid crystal display device according to claim 14, wherein the plurality of gate transmitting lines include at least eight signal lines for transmitting signals from the first printed circuit board to the second printed circuit board.

16. (Currently Amended) The liquid crystal display device according to claim 14,
wherein the plurality of gate transmitting lines includes a gate high voltage signal line and a gate low voltage signal line.

17. (Currently Amended) The liquid crystal display device according to claim 14, wherein the plurality of gate transmitting lines includes a common voltage signal line, a gate high voltage signal line, a gate low voltage signal line, first and second control signal lines, a power line and a ground line.

18. (Currently Amended) The liquid crystal display device of claim 14, further comprising a plurality of dummy pads between adjacent gate pads.

19. (Currently Amended) The liquid crystal display device of claim 14, further comprising a plurality of dummy pads between adjacent source pads.

20. (Currently Amended) A method of making a liquid crystal display device comprising: forming a liquid crystal panel including a first substrate and a second substrate, the first substrate having a plurality of source pads and gate pads, the first and second substrates being attached;

forming a first printed circuit board connected to the plurality of source pads, the first printed circuit board applying signals to the source pads;

forming a second printed circuit board connected to the plurality of gate pads, the second printed circuit board applying signals to the gate pads; and

forming a plurality of gate transmitting lines formed directly on the substrate and connecting the gate pads with the source pads, the plurality of gate transmitting lines transmitting signals from the first printed circuit board to the second printed circuit board via the gate transmitting lines, wherein the plurality of gate transmitting lines comprise two resistances.

21. (Currently Amended) The method according to claim 20, wherein one of the gate transmitting lines has a resistance of below 30 Ω , wherein and is capable of transmitting a gate low voltage is transmittable by the one of the gate transmitting lines.

22. (New) The method according to claim 1, wherein the first resistance is below 30 Ω .

23. (New) The method according to claim 1, wherein the second resistance is about 100 Ω .

24. (New) The method according to claim 1, wherein a gate low voltage is transmittable by the first gate transmitting line.